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Inventor: John T. Moor et al.

Title: Capacitor Constructions, DRAM Constructions and
Semiconductive Material Assemblies

Assignee: Micron Technology, Inc.

Serial No.: 09/603,147

Filed: June 23, 2000



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date:

1/22/01

Attorney:

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<h1>TRANSMITTAL FORM</h1> <p><i>(to be used for all correspondence after initial filing)</i></p>	Application Number	09/603,147	
	Filing Date	June 23, 2000	
	First Named Inventor	J. Moore	
	Group Art Unit	2822	
	Examiner Name	Unknown	
Total Number of Pages in This Submission		Attorney Docket Number	MI22-1443

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Remarks <p style="text-align: center;">NO OFFICE ACTION RECEIVED YET, THEREFORE <u>NO FEES</u>.</p>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	David G. Latwesen, Ph.D. #38,533 WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN, P.S.
Signature	
Date	1/22/01

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